



S D M Education Society's
Shree Dharmasthala Manjunatheshwara Institute of Technology (SDMIT)
Electrical & Electronics Engineering Department, Ujire – 574240, Karnataka – State, INDIA

TIME TABLE

VIII SEMESTER BE – EEE, Academic Year 2017-18, with effect from 01.02.2018

Lecture Hall: LH- 114

Class Advisor: Mrs. VEENA BHAT, Assistant Professor, EEE Dept

TIME DAYS	9.05AM to 10.00AM (1)	10.00AM to 10.55AM (2)	10.55AM to 11.10AM	11.10 AM to 12.05PM (3)	12.05 PM to 1.00 PM (4)	1.00PM to 2.15PM	2.15PM to 3.10PM (6)	3.10PM to 4.05PM (7)	4.05PM to 5.00PM (8)	
Monday	EADSM	EDEC	T E A B R E A K	RES	PSOC	L U N C H B R E A K	TECHNICAL SEMINAR			
Tuesday	RES	PSOC		EDEC	PPTC-AR		PSOC	EADSM		
Wednesday	EADSM	EDEC		RES	PSOC		MATLAB		FOSSEE LAB	
Thursday	PSOC	RES		EDEC	EADSM		BOSCH LAB			
Friday	TECHNICAL SEMINAR			TECHNICAL SEMINAR			BOSCH LAB			
Saturday	PROJECT WORK									

Sl No	Subject Code	Title	Name of the Faculty Member	Teaching hours/week		Examination			
				Theory	Practical	Duration in hours	Marks		
							IA	Theory/ Practical	Total
1	10EE81	Electrical Design, Estimating & Costing	Mr. Santosh P N	04	-	03	25	100	125
2	10EE82	Power System Operation & Control	Mr. Somashekar D P	05	-	03	25	100	125
3	10EE836	Elective-IV Renewable Energy Sources	Mr. Antheesh R	04	-	03	25	100	125
4	10EE842	Elective-V Energy Auditing & Demand Side Management	Mrs. Vineeth Kumar	04	-	03	25	100	125
5	10EEP85	Project Work Coordinators	Mr. Pradeep K.A	-	06	03	100	100	200
6	10EES86	Technical Seminar	Mr. Pradeep K.A	-	03	-	50	-	50
7		Pre placement programming	Mr. Antheesh R	01					
		MATLAB CLASS	Mr. Somashekar D P	01					
8		FOSSEE LAB	Mr. Antheesh R Mr. Vineeth Kumar		03				
9		BOSCH LAB	Mr. Vishwanath G S		06				
Total				19	18	15	250	500	750

TIME TABLE CO-ORDINATOR

HOD

PRINCIPAL



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Electrical & Electronics Engineering Department, Ujire – 574240, Karnataka – State, INDIA

TIME TABLE

VI SEMESTER BE – EEE, Academic Year 2017-18, with effect from 01.02.2018

Lecture Hall: LH- 408

Class Advisor: Mr. ANTHEESH R, Assistant Professor, EEE Dept

TIME DAYS	9.05AM to 10.00AM (1)	10.00AM to 10.55AM (2)	10.55AM to 11.10AM	11.10 AM to 12.05PM (3)	12.05 PM to 1.00 PM (4)	1.00PM to 2.15PM	2.15PM to 3.10PM (6)	3.10PM to 4.05PM (7)	4.05PM to 5.00PM (8)
Monday	EMD	PSA-1	T E A B R E A K	CS	S&T	L U N C H B R E A K	CS LAB B ₁ (SPN+DPS) DSP LAB-B ₂ (PKA+AS)		
Tuesday	EMD			CS	DSP		CAED LAB (VB+AR)		
Wednesday	PSA-1	CAED		S&T	CS		DSP		
Thursday	DSP	CS		PSA-1	EMD		CS LAB B ₂ (SS+SPN) DSP LAB-B ₃ (PKA+AS)		
Friday	DSP	CAED LAB (VB+AR)					CS LAB B ₃ (SS+SPN) DSP LAB-B ₁ (PKA+VK)		
Saturday	PSA-1	PPTC-VK		-----	EMD		S&T		

Sl. No	Subject Code	Title/Subject	Name of Faculty Member	Teaching hours/week		Examination			
				Theory	Practical	Duration in hours	Marks		
							IA	Theory/ Practical	Total
1	15EE61	Control Systems	Mrs. Sujatha. S	04	-	03	20	80	100
2	15EE62	Power System Analysis -1	Mr. Somashekar D P	04	-	03	20	80	100
3	15EE63	Digital Signal Processing	Mr. Avinash S	05	-	03	20	80	100
4	15EE64	Electric Machine Design	Dr. K. Manjunath	05	-	03	20	80	100
5	15EE651	Computer Aided Electrical Drawing	Mrs. Veena Bhat	01	06	03	20	80	100
6	15EE662	Sensors and Transducers	Mr. Subrahmanya Bhat	03	-	03	20	80	100
7	15EEL67	Control System Laboratory	Mrs. Sujatha. S Mr. Santosh P.N Mr. Somashekar D P	-	03	03	20	80	100
8	15EEL68	Digital Signal Processing Laboratory	Mr. Pradeep K.A Mr. Avinash S Mrs. Veena Bhat	-	03	03	20	80	100
9	--	Pre Placement Training Class	Mr. Vineeth Kumar	01					
Total				23	12	24	160	640	800

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IV SEMESTER BE – EEE, Academic Year 2017-18, with effect from 01.02.2018

Lecture Hall: LH- 113

Class Advisor: Mr. Subrahmanya Bhat Assistant Professor, EEE Dept

TIME DAYS	8.45AM to 9.45AM (1)	9.45AM to 10.45AM (2)	10.45AM to 11.00AM	11.00 AM to 12.00PM (3)	12.00PM to 1.00PM (4)	1.00PM to 2.15PM	2.15PM to 3.15PM (6)	3.15PM to 4.15PM (7)	4.15PM to 4.45PM (8)
Monday	EM-2	LIC	T E A B R E A K	FT	T&D	L U N C H B R E A K	LIC LAB B ₁ (VGS+VB) EM-2 LAB B ₂ (SB+VK)		
Tuesday	FT	M-4		PGEC	LIC		LIC LAB B ₂ (VGS+PKA) EM-2 LAB B ₁ (SB+SP)		
Wednesday	EM-2	M-4		FT	T&D		LIC	PGEC	
Thursday	LIC	PPTC-VK		EM-2	FT		PGEC	DIP MATHS-2/ FOSSEE LAB	
Friday	PGEC	T&D		M-4	MATLAB		EM-2	DIP MATHS-2	
Saturday	FT	LIC		T&D	M-4				

Sl No	Subject Code	Title/Subject	Name of the Faculty Member	Teaching hours/week		Examination			
				Theory	Prac tical	Durati on in hours	Marks		
							IA	Theory/ Practical	Total
1	15MAT41	ENGINEERING MATHEMATICS-IV	Ms. Divyashree P	04	-	03	20	80	100
2	15EE42	POWER GENERATION & ECONOMICS	Mr. Sathyanarayana P	04	-	03	20	80	100
3	15EE43	TRASMISSION & DISTRIBUTION	Mr. Santhosh P N	04		03	20	80	100
4	15EE44	ELECTRIC MOTORS -2	Mr. Subrahmanya Bhat	04	-	03	20	80	100
5	15EE45	ELECTROMAGNETIC FIELD THOERY	Mr. Pradeep K A	05	-	03	20	80	100
6	15EE46	OPERATIONAL AMPLIFIERS & LINEAR IC'S	Mr. Vishwanath G S	05	-	03	20	80	100
7	15EEL47	ELECTRICAL MACHINES LABORATORY -2	Mr. Subrahmanya Bhat Mr. Satyanarayana P	-	03	03	20	80	100
8	15EEL48	OP-AMP & LINEAR IC'S LABORATORY	Mr. Vishwanath G S Mrs. Veena Bhat Mr.	-	03	03	20	80	100
9	----	Pre Placement Training Class	Mr. Vineeth Kumar	01	-				
10	----	Matlab Class	Mr. Somashekar D P	01					
10	----	FOSSEE Laboratory	Mr. Antheesh R Mr. Vineeth Kumar	-	03				
Total				28	09	24	160	640	800

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