## YASHAVANTHA D G



#### **PROFILE SUMMARY**

**Assistant Professor** with over 18 years of experience in teaching and industry.

#### **CONTACT DETAILS**

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#### **SKILLS**

- C,C++
- Excel, MS Word, PowerPoint



## **SDM Institute of Technology, Ujire**

**Department of Electronics and Communication Engineering** 

### **EXPERIENCE**

- · ASSISTANT PROFESSOR at SDMIT, Ujire
- Assistant Professor at SDIT, Mangalore
- MANAGER FIELD SUPPORT
- SR.ENGINEER TECHNICAL SUPPORT
- SR.ENGINEER TECHNICAL SUPPORT
- ENGINEER TECHNICAL SUPPORT
- ENGINEER TECHNICAL SUPPORT

Sep-2024 to Present Jan-2024 to May-2024 Jan-2009 to Dec-2011 Apr-2006 to Sep-2008 Dec-2004 to Mar-2006 Apr-1997 to Mar-2004 Jun-1994 to Mar-1997

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### **EDUCATION**

- M.TECH. IN VLSI DESIGN AND EMBEDDED SYSTEMS at KVG College of Engineering (Sullia)

  2014
- B.E. IN ELECTRONICS AND COMMUNICATION ENGINEERING at KVG College of Engineering (Sullia) 1993

# WORKSHOPS/FDPS ATTENDED IN LAST 5 YEARS

• FDPs:

## SUBJECTS HANDLED

Introduction to Electronics, Digital Logic Circuits, Introduction to C++ Programming, Embedded Systems Application.