Mr. Jagadeesha M

Profile Summary

Assistant Professor with over 16 years of experience in teaching.

Contact Details

@ jagadeesh.sullia@gmail.com
@ jagadeesh.sullia@sdmit.in
    +919901730364

Research Profile

Persuing PhD

Research Metrics

Skills

- Verilog, VHDL, Python, C, C++, Java.
- Excel, MS Word, PowerPoint

SDM Institute of Technology, Ujire
Department of Information Science and Engineering

Experience

- Assistant Professor at ECE Department , KVGCE Sullia for 15 years.
- Assistant Professor at ISE Department, SDMIT Ujire from 12th January 2024 to till date.

Education

- M.Tech. in VLSI Design and Embedded Systems at KVGCE, Sullia   **2012**
- B.E. in ELectronics and Communication Engineering at *KVGCE Sullia*   **2008**

Research and PubLications

- Number of Quality Journal Published   **1**
- Number of Conference Papers   **1**
- Number of Patents Granted/Published   **1**

Workshops/FDPs Attended in Last 5 Years

- FDPs: **5**
- Workshops: **3**

Subjects Handled

Software Engineering, Machine Learning, Big data Analysis, Computer Networks, Innovation and Design Thinking, UI/UX, Digital Design and Computer Organization, Introduction to C, Introduction to C++, Electromagnetic waves, Signals and Systems, Network Analysis, 8051 Microcontroller, Data structure using C++, Basic Signal Processing, ARM Microcontroller and Embedded System, Control system, Error Control Coding, Analog and Mixed mode VLSI design, Antennas and wave propagation, Analog communication.

MisceLLaneous

- Departmental Placement Coordinator
- Activity Coordinator
-