

MR.JAGADEESHA M.



PROFILE SUMMARY

Assistant Professor with over 16 years of experience in teaching and research.

CONTACT DETAILS

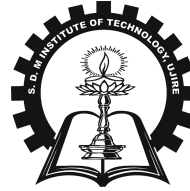
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RESEARCH PROFILE

Pursuing PhD

SKILLS

- MATLAB, Python,C/C++ programming, JAVA programming , AI Tools
- LaTeX, Excel, MS Word, PowerPoint



SDM INSTITUTE OF TECHNOLOGY, UJIRE

Department of Information Science and Engineering

EXPERIENCE

- ASSISTANT PROFESSOR at *SDM Institute of Technology (Ujire)*
Jan-2024 to Present
- LECTURER at *KVG College of Engineering (Sullia)*
Jan-2009 to Sept-2014
- ASSISTANT PROFESSOR at *KVG College of Engineering (Sullia)*
Sept-2014 to Jan-2024

EDUCATION

- M.TECH. IN VLSI DESIGN AND EMBEDDED SYSTEMS at *KVG College of Engineering (Sullia)* **2014**
- B.E. IN ELECTRONICS AND COMMUNICATION ENGINEERING at *KVG College of Engineering (Sullia)* **2008**

RESEARCH AND PUBLICATIONS

- Number of Conference Papers **2**
- Number of Patents Granted/Published **1**

WORKSHOPS/FDPS ATTENDED IN LAST 5 YEARS

- FDPS: **4**
- Workshops: **5**

SUBJECTS HANDLED

Machine learning, Computer Networks, Big Data Analytics, Digital Design and Computer organization, Cloud Computing, Software Engineering, Innovation and Design Thinking, Introduction to C, Introduction to C++, Electromagnetic waves, Signals and Systems, Network Analysis, 8051 Microcontroller, Data structure using C++, Basic Signal Processing, ARM Microcontroller and Embedded System, Control system, Error Control Coding, Analog and Mixed mode VLSI design, Antennas and wave propagation, Analog communication